

Abstract**Delay locked loop and a method for delay control**

The invention creates a delay control apparatus for provision of clock signals in circuit units having a delay device (100) comprising a first delay element (101) for provision of a variable time delay (105) between an output signal (104) and an input signal (103) for the first delay element (101), a feedback device (106) for feeding back the output signal (104) and a phase difference detection device (108) for detection of a phase difference between the input signal (103) and the fed-back output signal (107). The phase difference detection device emits a control signal (109) for controlling the first delay element (101) as a function of the detected phase difference, while a further, second delay element (102) which is connected in series with the first delay element (101), produces an additional delay as a function of a frequency of the output signal (103) which is detected by a frequency detection unit (110).

(Figure 2)